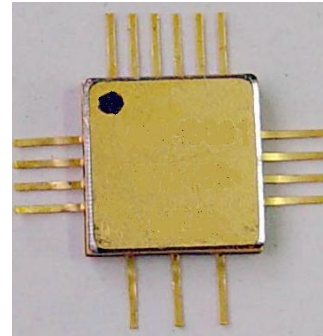


Performance

- Frequency: 2.7~3.5GHz
- Phase Error (RMS): 1°
- Insertion loss: -5dB
- Amplitude Equalization: ± 0.3 dB
- Operating Voltage: ± 5 V, TTL
- Package Form: Metal Ceramic Package Q17-01H
- Package Size: 10mm*10mm*2mm
- Internal Integrated Driver

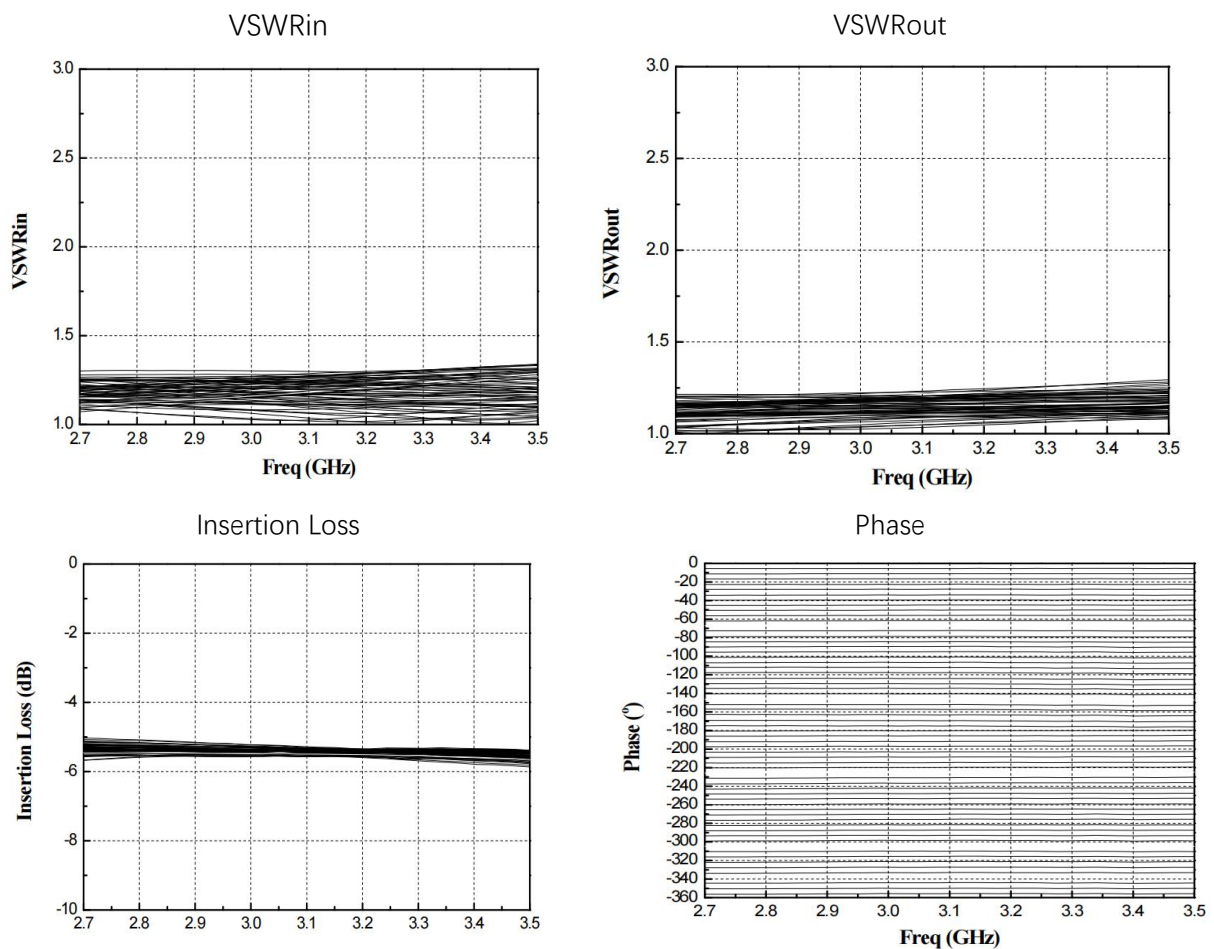
Function Diagram

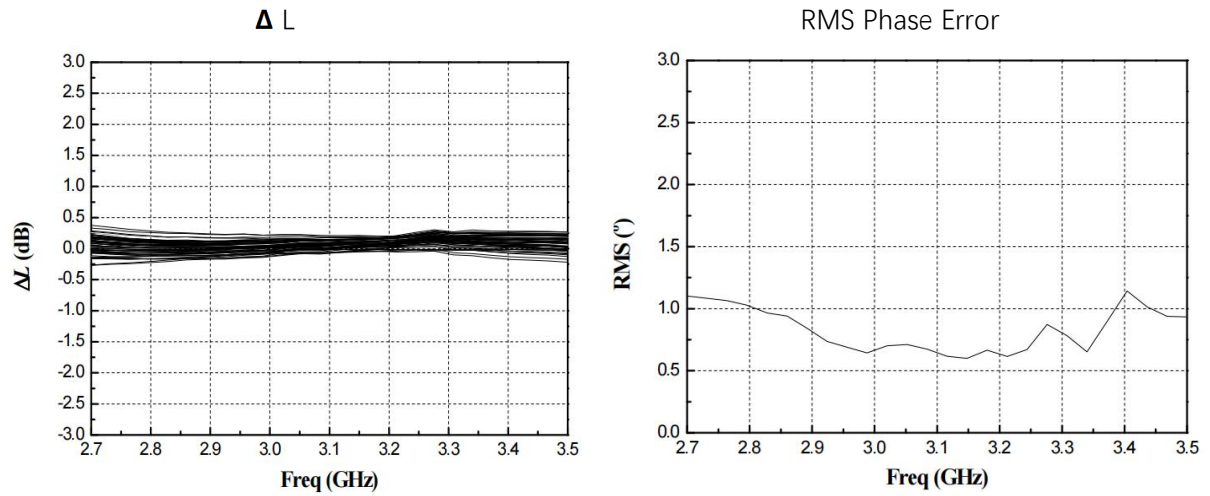


Electrical Specifications (TA=25°C, Vee:-5V,Vcc:+5V,V1,2.....:TTL,F: 2.7~3.5GHz)

Symbol	Parameter	Min	Typical	Max	Unit
Li	Insertion Loss	-	5	5.8	dB
RMS	RMS Phase Error	-	0.8	1.5	°
Δ Li	Amplitude Equalization	-	0.3	0.5	dB
VSWR	VSWR in/out		1.3	1.5	

Test Curves





Absolute Max Ratings (T_A=25°C)

Symbol	Parameter	Value	Remark
V1~V6	Control Voltage	TTL	
Pin	Input Power	25dBm	
Tch	Channel Temperature	150°C	1
Tm	Mounting Temperature	220°C	
Tstg	Storage Temperature	-55~125°C	

Exceeding any one or combination of these limits may cause permanent damage.

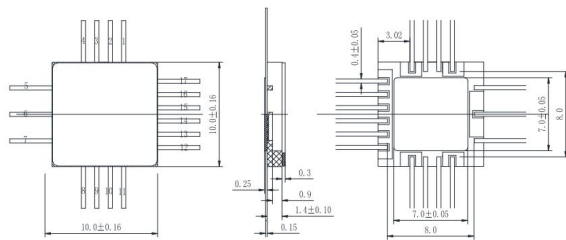
Truth Table

Pin	11	12	1	17	16	15	14	13
Function	Vee	Vcc	V1	V2	V3	V4	V5	V6
Ground State	-5V	+5V	0	0	0	0	0	0
5.625	-5V	+5V	0	0	0	1	0	0
11.25	-5V	+5V	0	1	0	0	0	0
22.5	-5V	+5V	0	0	1	0	0	0
45	-5V	+5V	0	0	0	0	1	0
90	-5V	+5V	1	0	0	0	0	0
180	-5V	+5V	0	0	0	0	0	1

Pads Definition

No	Port	Description	No	Port	Description
1	V1	90° Ground state control bit	10	GND	Ground
2	GND	Ground	11	Vee	Power Supply Port (-5V)
3	GND	Ground	12	Vcc	Power Supply Port (+5V)
4	RFin	RF signal input port	13	V6	180° control bit
5	GND	Ground	14	V5	45° control bit
6		No Connected	15	V4	5.625° control bit
7	GND	Ground	16	V3	22.5° control bit
8	RFout	RF signal output port	17	V2	11.25° control bit
9	GND	Ground			

Outline Drawing (mm)



Control Voltage

State	Bias
Low (0)	0-0.2V
High (1)	3-5V