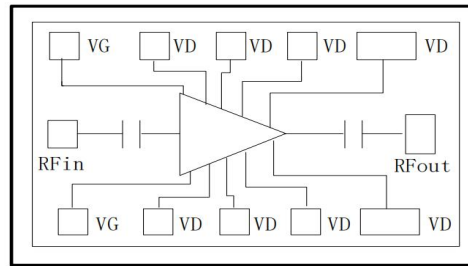


Performance

- Frequency: 18~27GHz
- Typical Signal Gain: 17dB
- Typical Pout: 31dBm
- Typical PAE: 25%
- Bias: -0.6V, 5V
- Technology: 0.15um pHEMT
- Size: 2.72mm*2.4mm*0.08mm

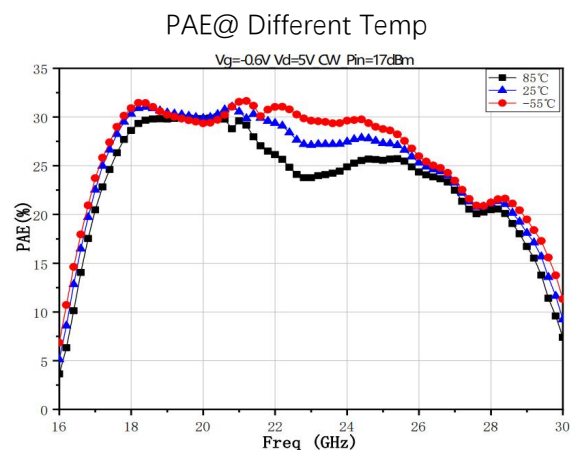
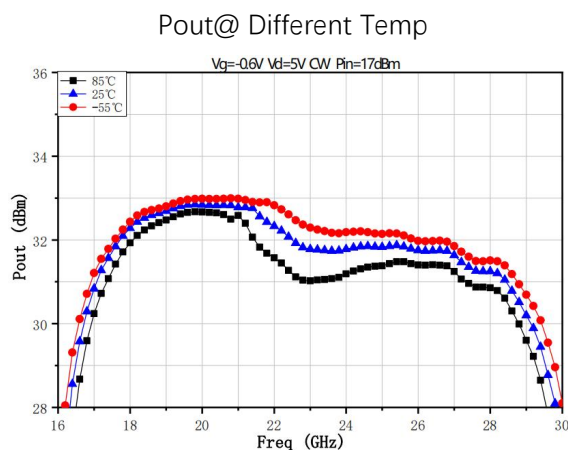
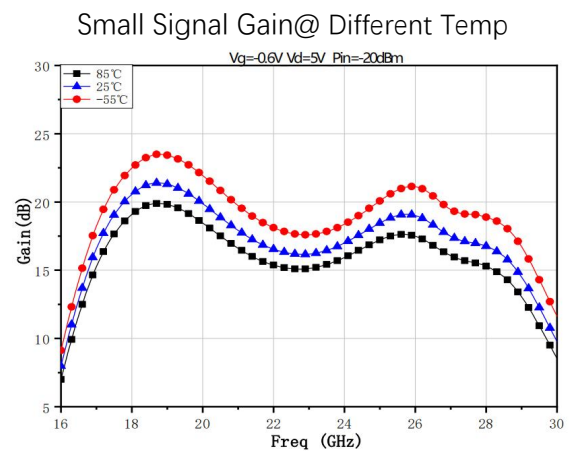
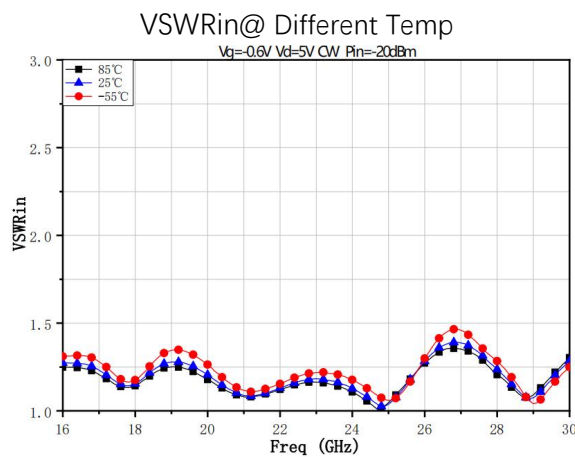
Function Diagram



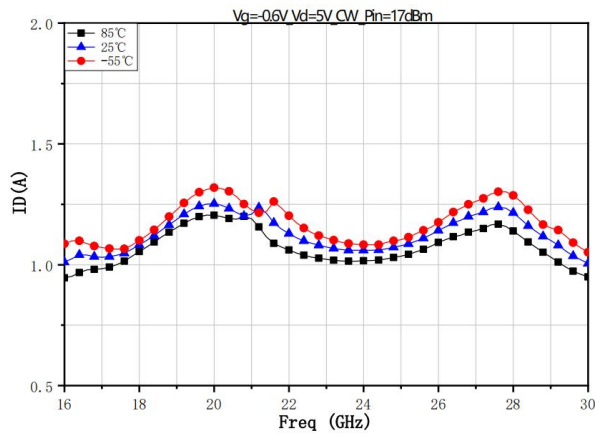
Electrical Specifications (TA=25°C, Vd=5V, Vg=-0.6V, F: 18~27GHz)

Symbol	Parameter	Min	Typical	Max	Unit
G	Small Signal Gain	-	17	-	dB
Gp	Power Gain	-	14.5	-	dB
Pout	Saturated Power	-	31	-	dBm
Id	Dynamic Current	-	1.1	-	A
PAE	Power Added Efficiency	-	25	-	%

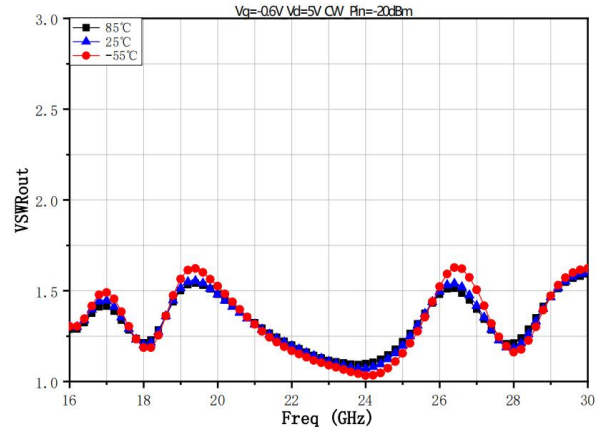
Test Curves



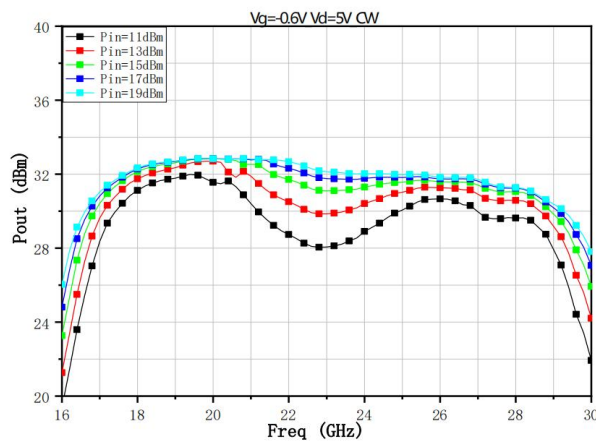
ID@ Different Temp



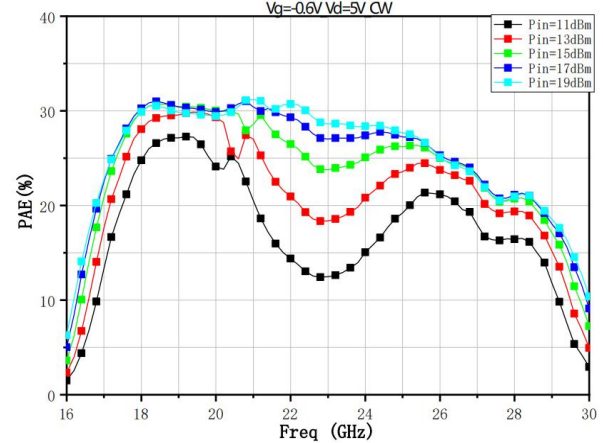
VSWRout@ Different Temp



Pout@Different Pin



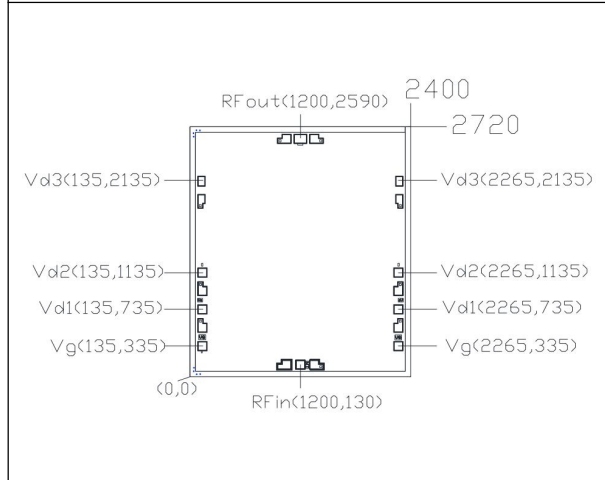
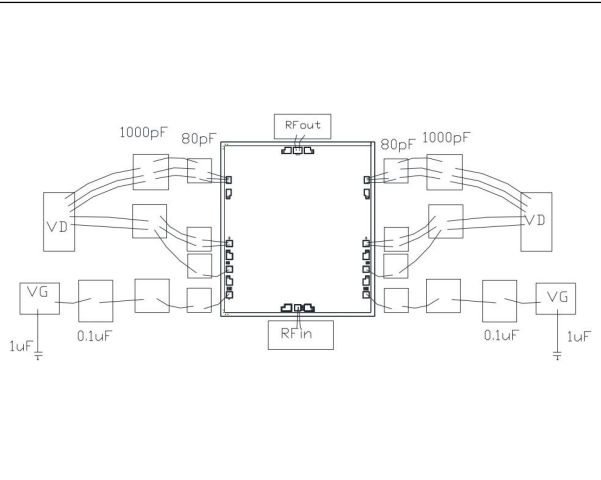
PAE@Different Pin



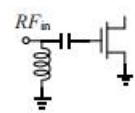
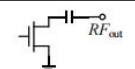
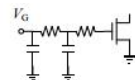
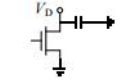
Absolute Max Ratings (TA=25°C)

Symbol	Parameter	Value	Remark
Vd	Drain Voltage	6V	
Id	Drain Current	2A	
Pd	DC Power	10W	
Pin	Input Power	25dBm	
Tch	Channel Temperature	150°C	
Tm	Mounting Temperature	310°C	30 s, N2 Protection
Tstg	Storage Temperature	-65~150°C	

Exceeding any one or combination of these limits may cause permanent damage.

Outline Drawing	Assembly Drawing
	

Pads Definition

Pad	Description	Equivalent Circuit
RFin	RF Signal input, connect to 50ohm system, block capacitor is needed if there's external DC applied on this pad.	
Rfout	RF Signal output, connect to 50ohm system, no need block capacitor.	
VG	Amp gate bias, external 150pF, 1000pF, 10uF capacitor is needed	
VD1、VD2	Amp drain bias, external 150pF, 1000pF capacitor is needed	
GND	Bottom must connect to RF and DC ground	