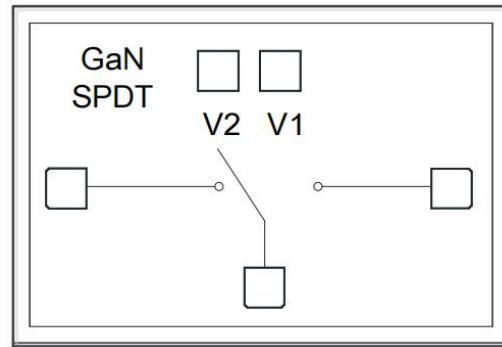


Performance

- Frequency: 70~100GHz
- Insertion loss: 2.1dB
- Isolation: 32dB
- Input P-0.3: >33dBm
- Input/Output VSWR: 1.8:1
- Switching Time: 10ns
- Control Mode: 0/-15V
- Voltage control reflective switch
- Chip size: 1.7*0.65*0.08mm

Function Diagram



Electrical Specifications (Ta=+25°C, V1~V2=0V/-15V, F=70~100GHz)

Symbol	Parameter	Min.	Typ.	Max.	Unit
Li	Insertion Loss	-	2.1	-	dB
ISO	Isolation	-	32	-	dB
VSWR	Input / Output VSWR	-	1.8	-	-

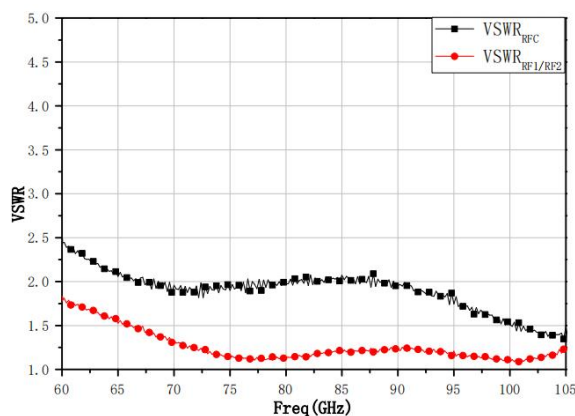
Absolute Maximum Ratings (Ta=+25°C)

Symbol	Parameter	Value	Notes
V1,2	Control Voltage	0.5/-30V	
Pcw	Input Continuous Wave Power	>33dBm	
Tch	Channel Temperature	225°C	
T	Sintering Temperature	310°C	30s, N2 protection
Tstg	Storage Temperature	-65~150°C	

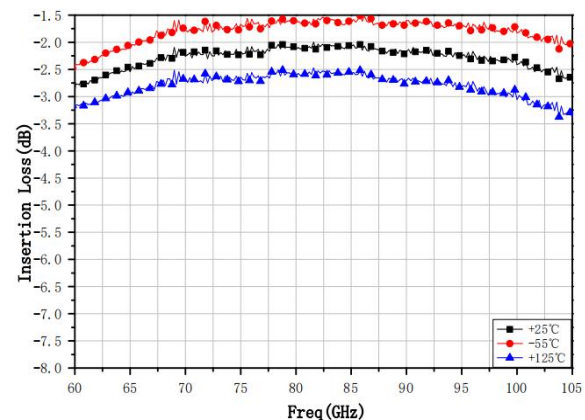
Exceed any of above listed ratings may cause permanent damage.

Test Curves (Ta=+25°C, (V1,2=-15V))

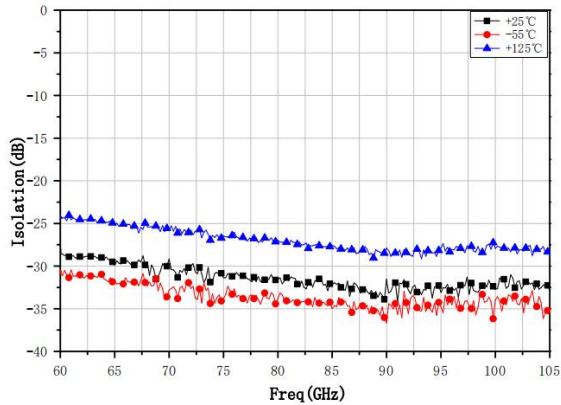
VSWR



Insertion Loss @ Different temperature



Isolation @ Different temperature



Outline Size (μm)	Assembly Diagram

Note: 1, RF input/output dual wire length is less than 300um.

2, The control signals of the same pressure point are the same and all need to be pressed.

PIN Definition

PIN	Description
RF1、RF2	RF ports, 50Ω, no block capacitor
RFc	RF common ports, 50Ω, no block capacitor
V1、V2	Control Ports
GND	Bottom must be grounded

Truth Table

V1	V2	RFc→RF1	RFc→RF2
1	0	OFF	ON
0	1	ON	OFF

Note: 1. Low level "0" is -10V~-20V, High level "1" is 0V~0.5V

2. Performance is same under condition of -10V ~ -20V