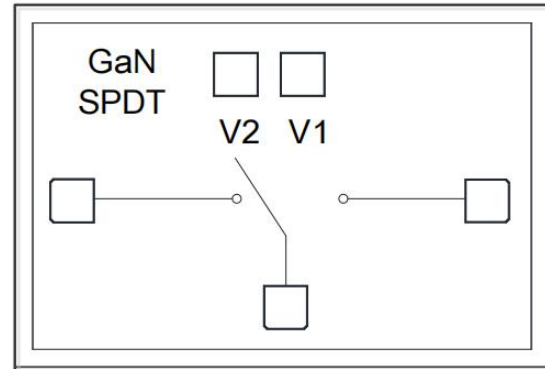


Performance

- Technology: 0.25um GaN FET
- Frequency: DC~6GHz
- Insertion loss: 0.8dB
- Isolation: 38dB
- Input P-0.3: 48dBm
- Input/Output VSWR: 1.5:1
- Switching Time: 20ns
- Control Mode: 0/-40V
- Voltage control reflective switch
- Chip size: 1.6*0.85*0.08mm

Function Diagram



Electrical Specifications (Ta=+25°C, V1~V2=0V/-40V, F=DC~6GHz)

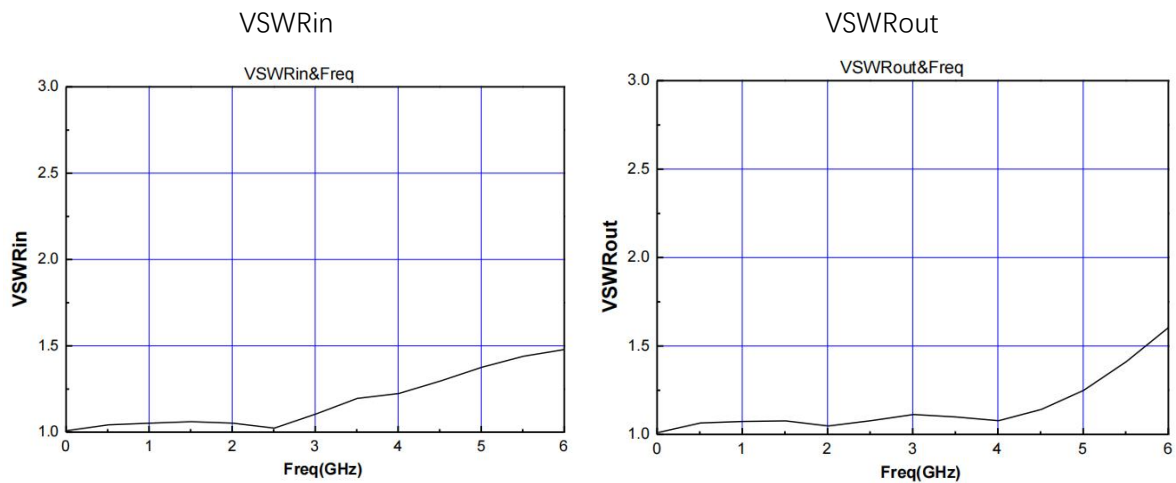
Symbol	Parameter	Min.	Typ.	Max.	Unit
Li	Insertion Loss	-	0.8	-	dB
ISO	Isolation	-	38	-	dB
VSWR	Input / Output VSWR	-	1.5	-	-

Absolute Maximum Ratings (Ta=+25°C)

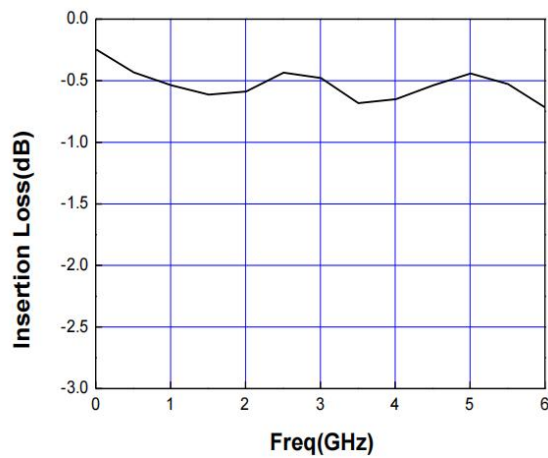
Symbol	Parameter	Value	Notes
V1,2	Control Voltage	-60V	
Pcw	Input Continuous Wave Power	49dBm	
Tch	Channel Temperature	225°C	
T	Sintering Temperature	310°C	30s, N2 protection
Tstg	Storage Temperature	-55~150°C	

Exceed any of above listed ratings may cause permanent damage.

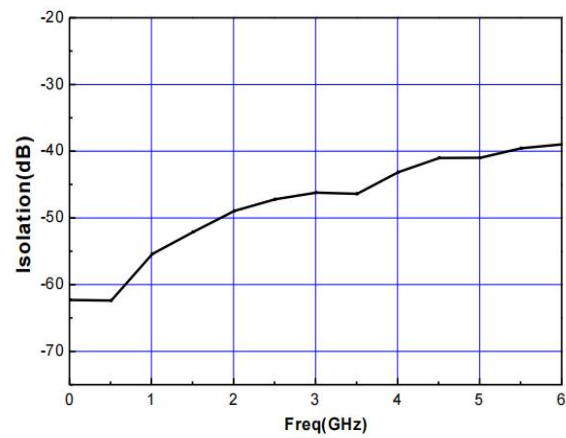
Test Curves (Ta=+25°C, (V1,2=-40V))



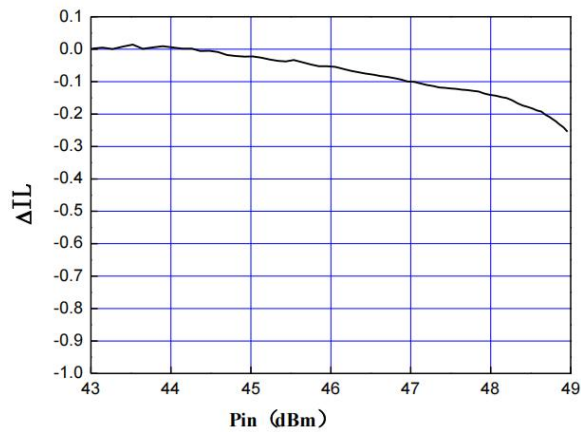
Insertion Loss



Isolation



Input Power (Y axis is normalized value of I.L)



Outline Size (μm)	Assembly Diagram
	The driver is connected to Vee or suspension on the back

Note: Recommended single wire length for RF port is >0.8mm, <1.2mm.

Dual wires length is >1mm, <1.5mm.

PIN Definition

PIN	Description
RF1、RF2	RF ports, 50Ω, no block capacitor
RFc	RF common ports, 50Ω, no block capacitor
V1、V2	Control Ports
GND	Bottom must be grounded

Truth Table

V1	V2	RF _c →RF1	RF _c →RF2
1	0	OFF	ON
0	1	ON	OFF

Note: Low level "0" is -28V~-40V, High level "1" is 0V~0.5V