



HL9333 Sampler / Harmonic Mixer IC

Features and Technical Specifications

RF Bandwidth (typ-ical)	DC to 17 GHz (-3dB) DC to 19 GHz (-6dB)
LO Input Frequency, Square Wave	100 MHz to 7 GHz t _/ /t _r = 50 ps (20-80%) max
LO Input Amplitude, Square Wave	300 mV $_{\rm pp}$ (600 mV $_{\rm pp}$ Diff) minimum
IF Bandwidth (typ.)	DC - 700 MHz (-3 dB)
Conversion Loss (LO = 1 GHz)	20 dB
LO to RF Isolation*	75 dB

Linearity, Second Harmonic Distortion** -68 dBc

Linearity, Third Harmonic Distortion** -66 dBc

Input IP3 (typ.)

PRODUCT SUMMARY

The HL9333 is a high-precision sampler / harmonic mixer integrated circuit offering excellent linearity, low noise and flat frequency response up to 20 GHz

(RF).

APPLICATIONS

version

· Harmonic down con-

High-speed front-end

• Reference design eval boards available

The following options are

HL9333- SMD package HL9333-EVAL-MA -

mounted to eval board

HL9333-EVAL-HL -

(1 MHz to 20 GHz)

mounted to eval board

with HYPERLABS balun

EXPORT RESTRICTIONS

An export license may be required to purchase this

product from outside of the United States. Please

contact HYPERLABS for more information.

with MACOM balun (2-18

for A/D converters

• Use in network analyzers, TDRs, sampling oscilloscopes, and spec-

trum analyzers

OPTIONS

available:

GHz)

28 dBm

-130 dBm/Hz

Input Noise Floor

Power Supplies

+6.0 V, 175 mA (VDD) +2.5 V, 110 mA (+2.5V)

-5.0 V, 320 mA (VSS)

+/- 5% Voltage Tolerance

Power Dissipation

3.0 W

Maximum Input Power +15 dBm

Dimensions

4.0 x 4.0 x 1.25 mm, 28 lead QFN

Packaging

Gel-Pak

Case Temperature

+85 °C, max operating

+245 °C, for 90 seconds max processing

RoHS Compliant

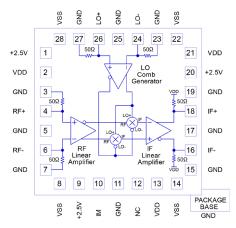
Yes

REACH Compliant

Yes



HL9333 4 x 4 mm QFN Package, 28 pin



HL9333 Port Assignments

^{*} NOTE: Assumes LO driven at 0 dBm through recommended limiting amplifier.

^{**} NOTE: Harmonic distortion measurements taken under test conditions: LO = 2 GHz square wave, RF = 100 MHz @ 0 dBm

HL9333 Downconversion Loss

Figure 1 shows the typical downcoversion loss (dB) at IF = 100 MHz.

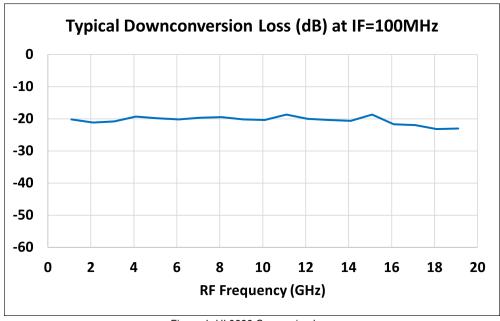


Figure 1: HL9333 Conversion Loss
Measured at LO = 1 GHz, IF = 100 MHz, -10 dBm RF Input Power

HL9333 Downconversion Harmonic Distortion

Figure 2 shows the typical downcoversion harmonic distortion (dBc) at IF = 100 MHz.

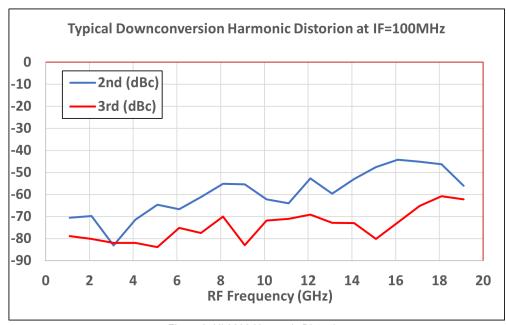


Figure 2: HL9333 Harmonic Distortion
Measured at LO = 1 GHz, IF = 100 MHz, -10 dBm RF Input Power